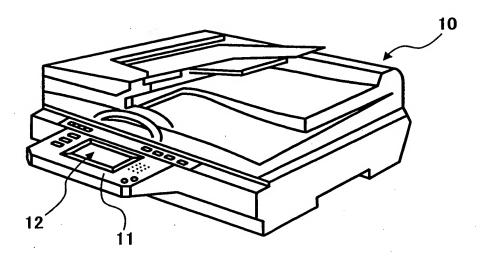
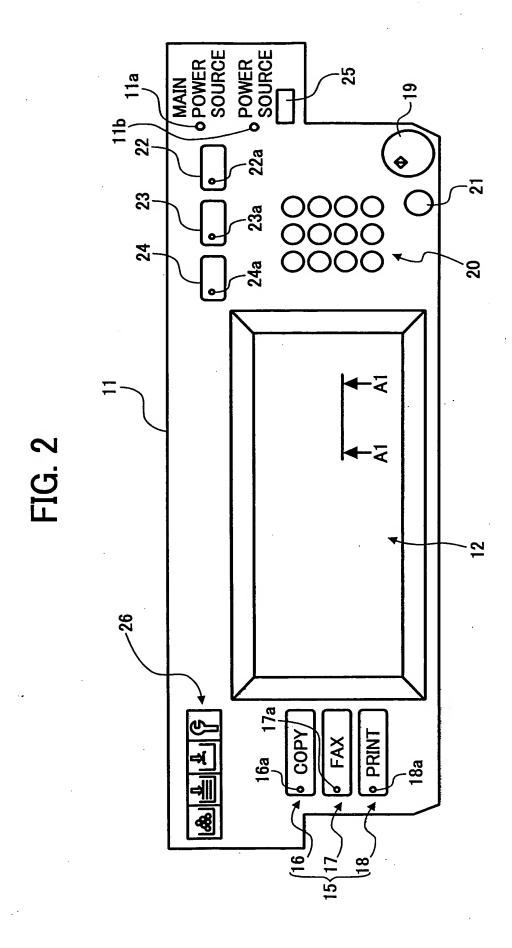
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FIG. 1





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FIG. 3

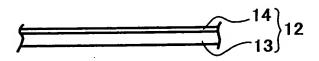
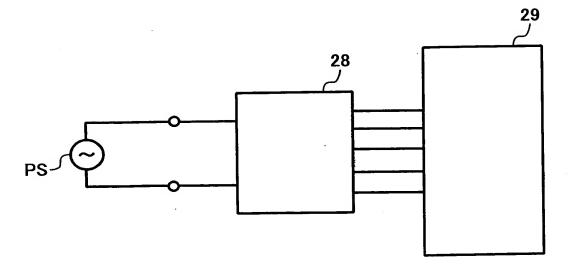
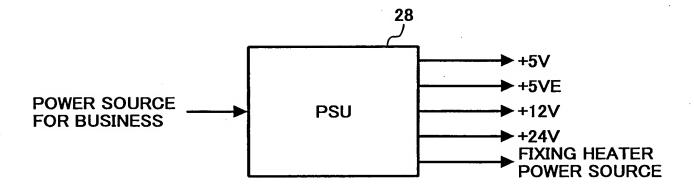


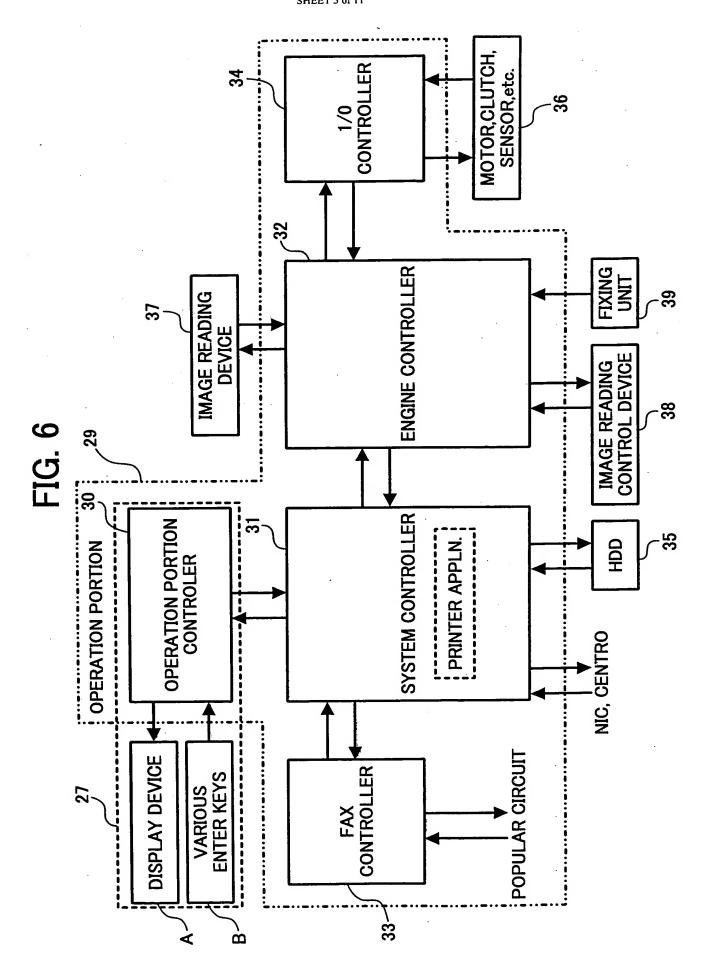
FIG. 4

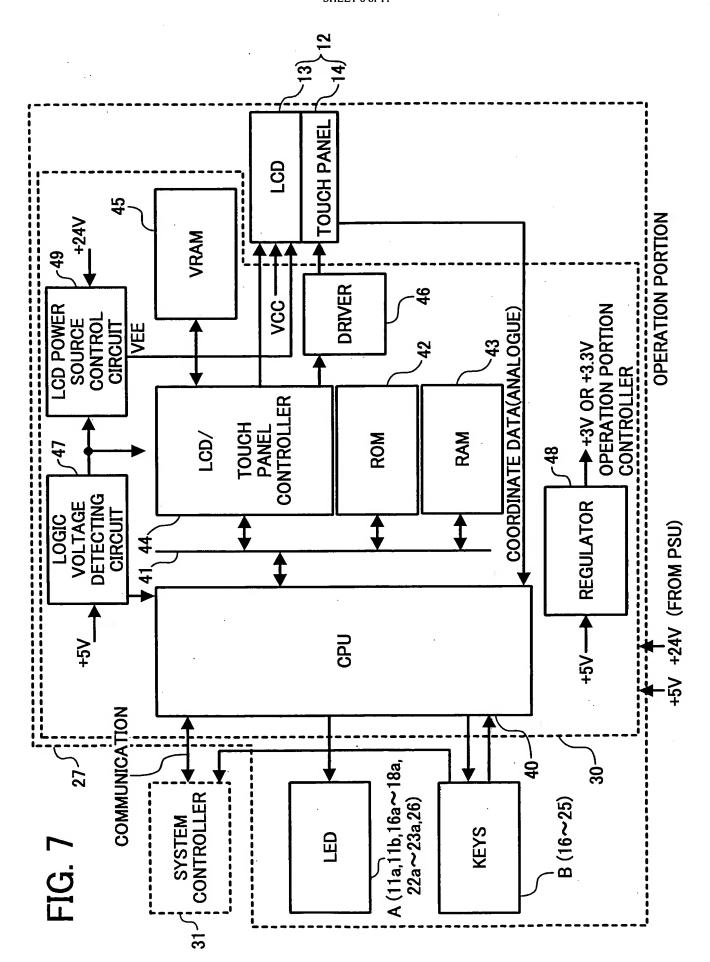


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FIG. 5







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FIG. 8A

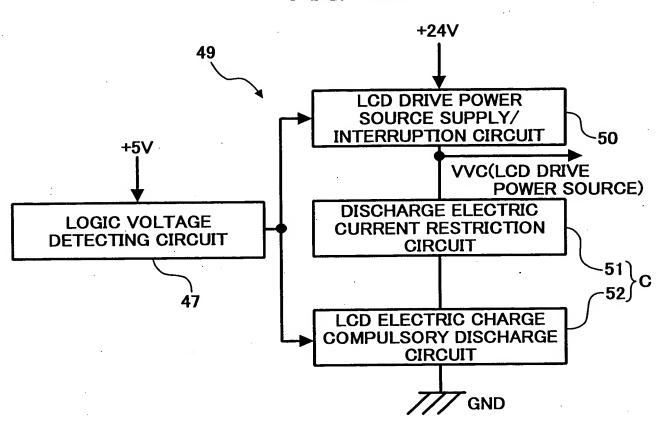
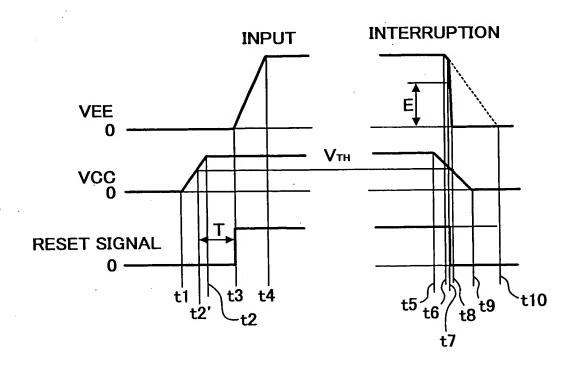
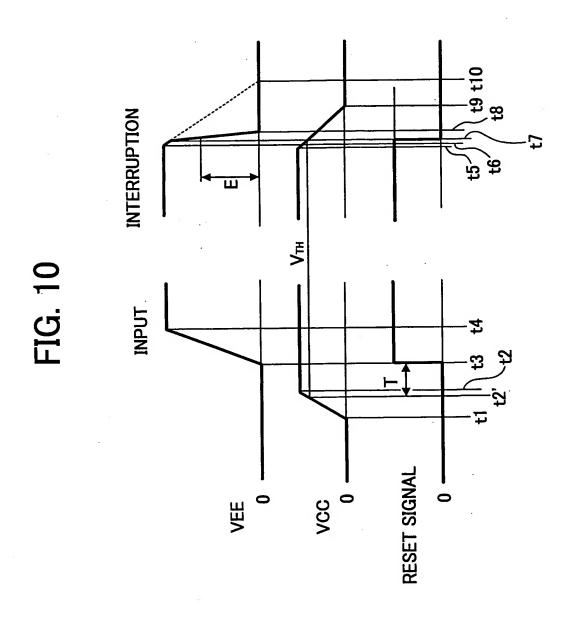


FIG. 8B



TT GND **Q**2 TO RESET TERMINAL OF CPU, etc. **R**4 2



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FIG. 11

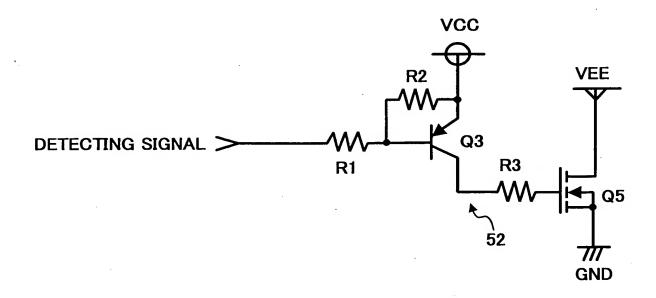


FIG. 12

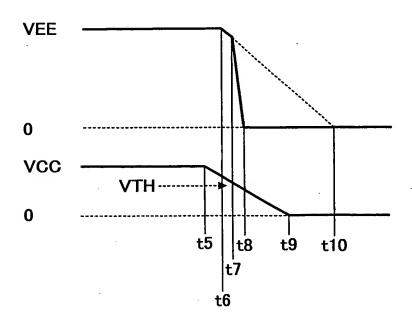


FIG. 13

